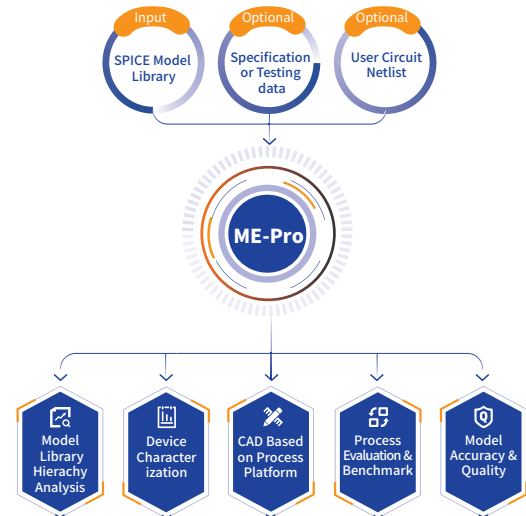


## Process and Design Verification Platform

### Introduction

ME-Pro is an innovative design verification platform that bridges IC design and semiconductor manufacturing process development, serving as a common verification platform for designers, process developers, modeling engineers and PDK engineers.

ME-Pro supports simulation analysis for semiconductor device model verification and evaluation, providing the technology platform that enables interactive development in the process, device and circuit areas. ME-Pro helps designers uncover the full potential of manufacturing process technology, effectively raises quality and competitiveness of circuit and product design, and provides feedback for process improvement. Leveraging decades of Primarius' industrial experiences, ME-Pro offers pre-configured templates with hundreds of verification data entries and includes the built-in parallel simulator NanoSpice to greatly improve efficiency. ME-Pro provides an easy configuration setup leading to a short learning curve, as well as the most complete and systematic capabilities for device, circuit, process evaluation and benchmarking for use by modeling engineers, CAD engineers, and circuit designers.



### Specifications

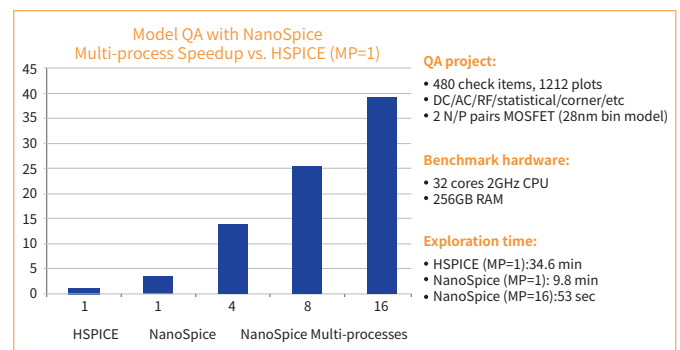
- Library browser for hierarchy display and review
- Model exploration for all device/circuit targets
- Device characteristics and trend evaluation and analysis
- Device and circuit correlation analysis
- Corner, statistical, LDE and aging model behavior evaluation
- Platform benchmarking for multi-foundries, nodes, platforms, or different process revisions
- Design assistant for user-specified designs
- Pre-configured template includes 200+ check items and settings
- Automatically and rapidly generate verification documents in various formats

### Applications

- Fabless: Foundry interface/COT team/CAD/Design team
- Foundries & IDM: SPICE model/Process development/Design service team/Circuit design team

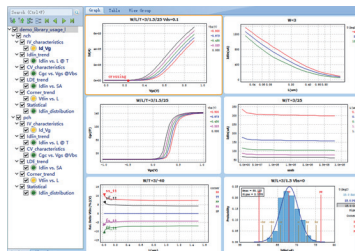
### Key Advantages

- Build-in parallel simulator NanoSpice, supports external simulator
- Rapidly analyze and validate SPICE model, ensure quality and accuracy
- Short learning curve for quick adoption and best use of process platform
- Systematic evaluation of any and all device/circuit targets
- Avoids misuse of models, improves design margins
- Rich default setting and flexibility significantly speed up adoption
- Intuitive GUI, easy to configure: Run verification projects in minutes

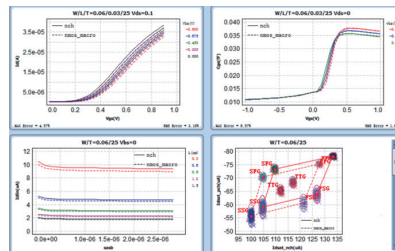


### Application Examples

#### SPICE Model Validation



#### Technology Platform Evaluation



#### Circuit Behaviour Evaluation

